

Interconnected Fabric Module

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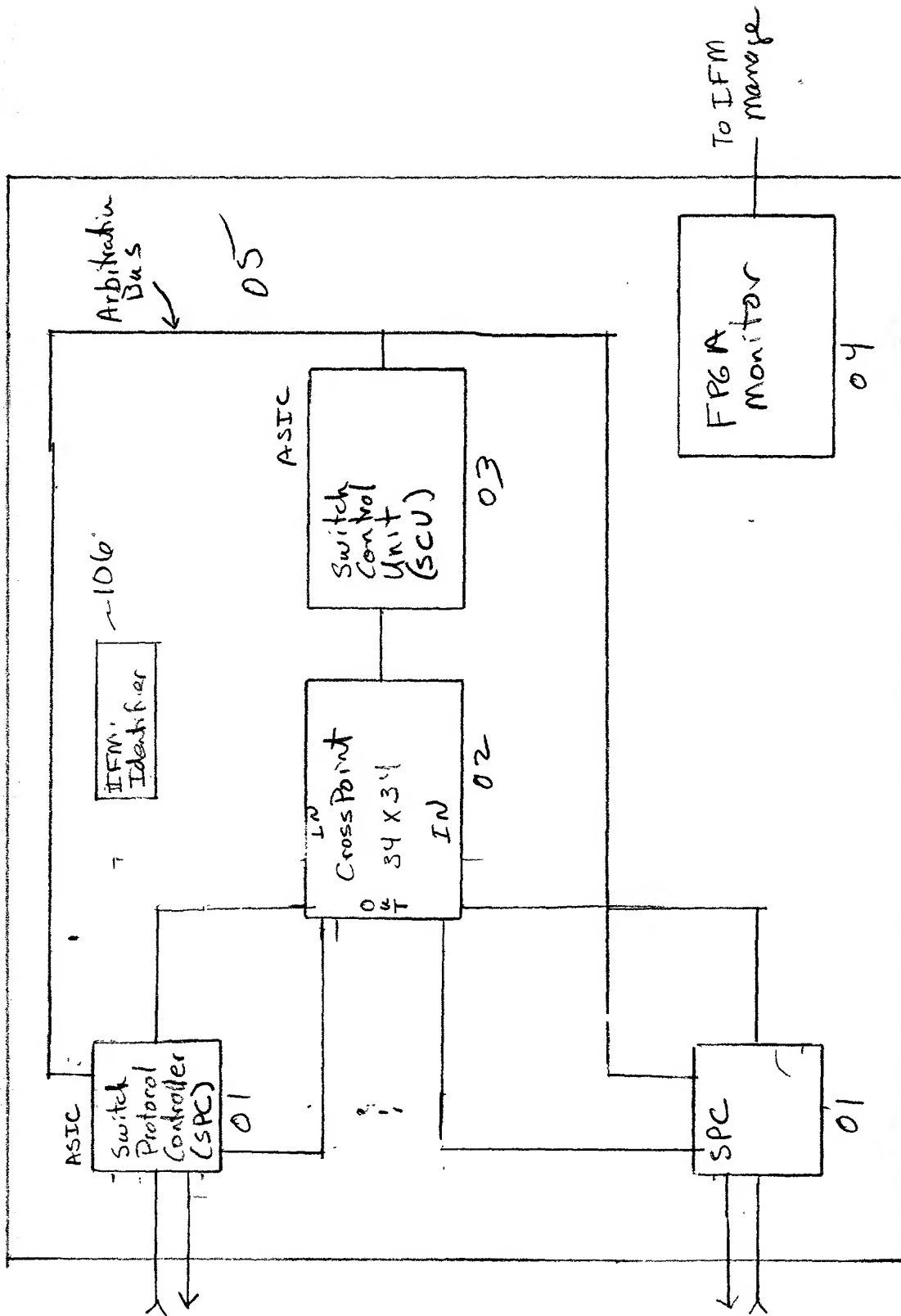


Fig 1

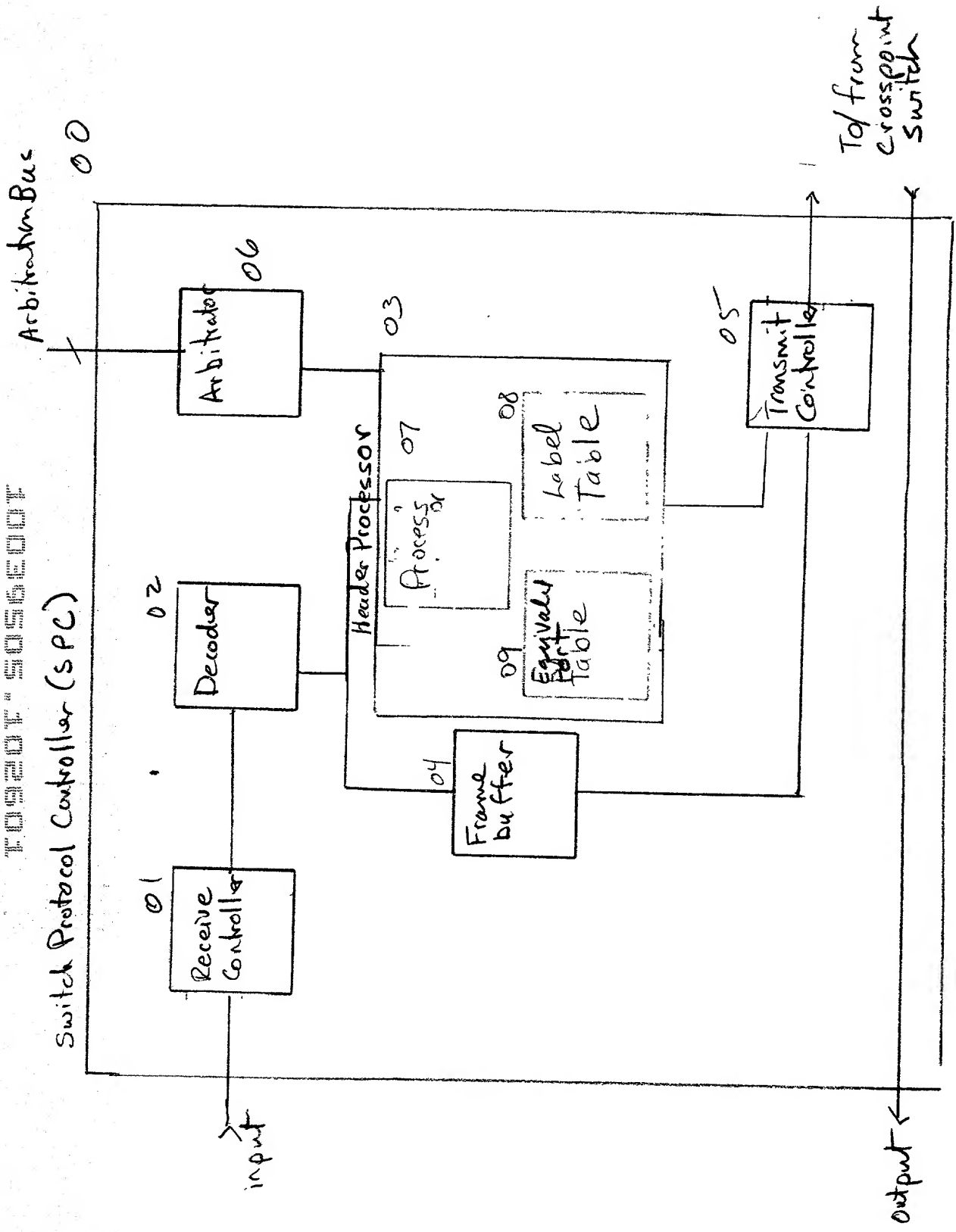


Fig 2

Label Table Segment

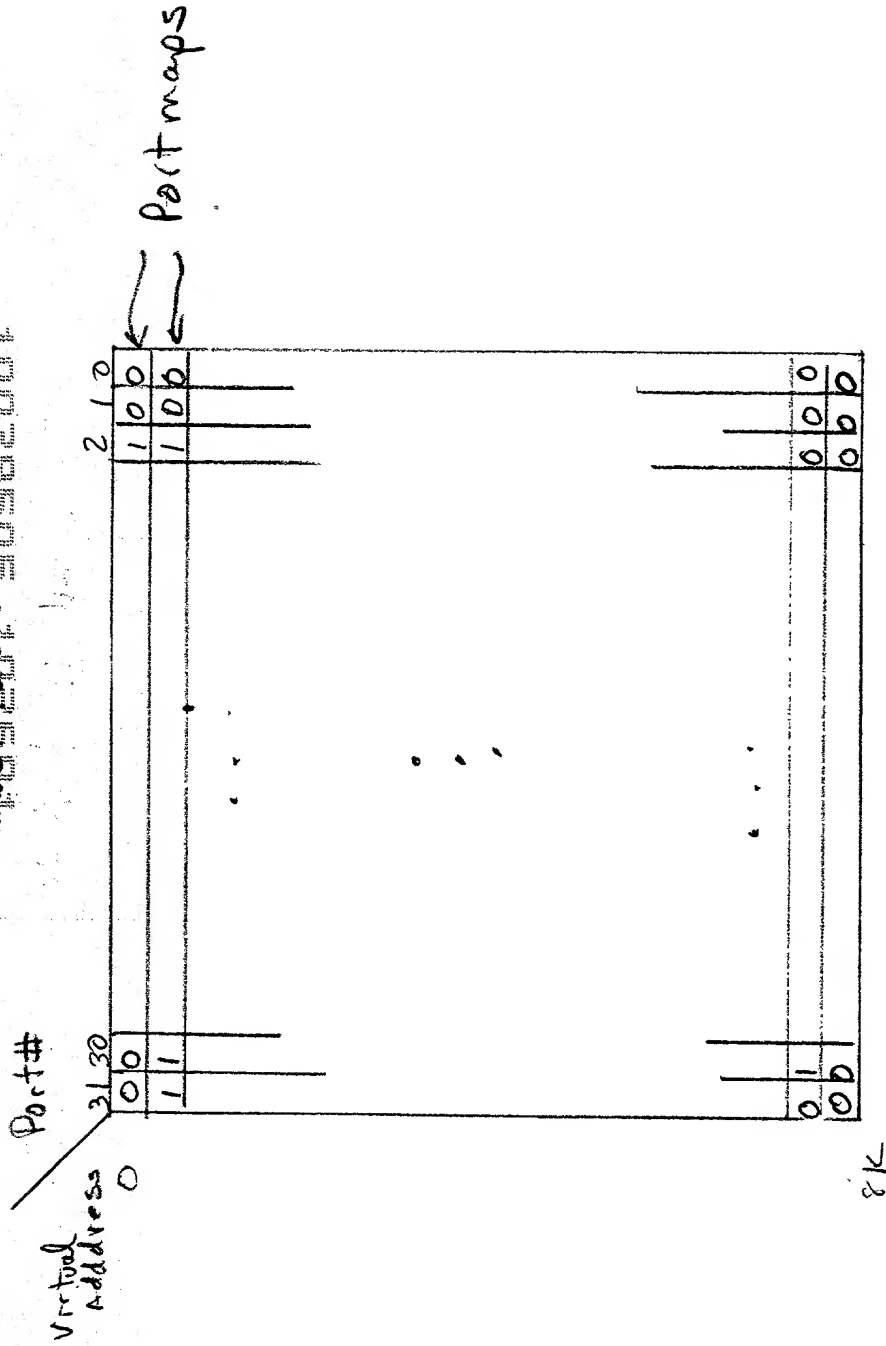


Fig 3

Frame

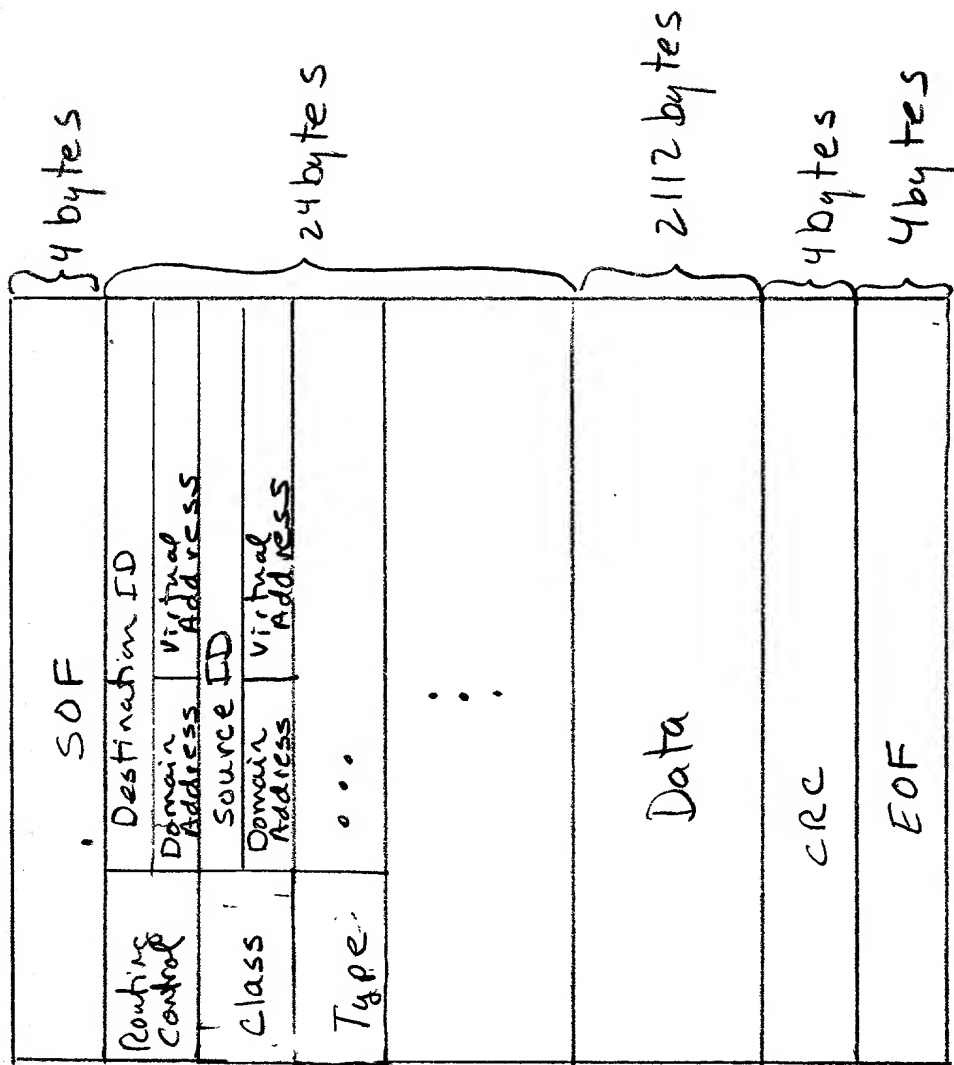


Fig 4

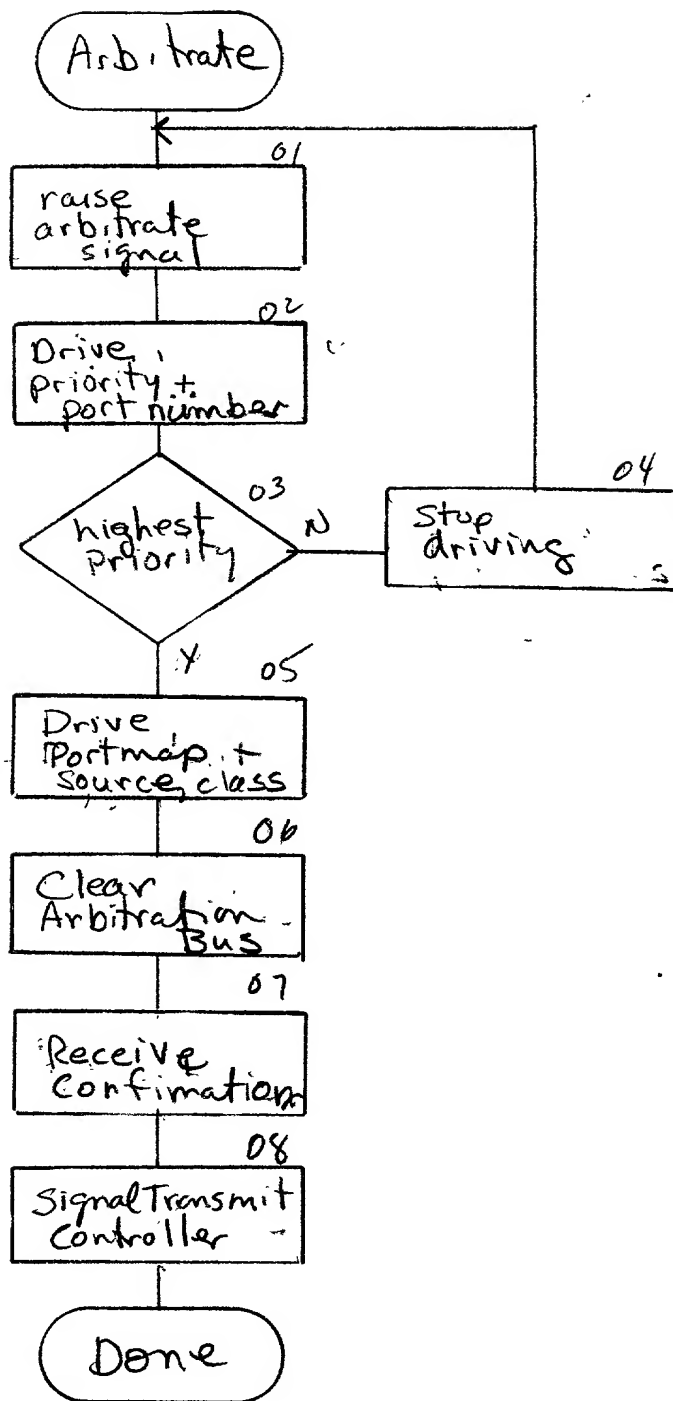


Fig 5

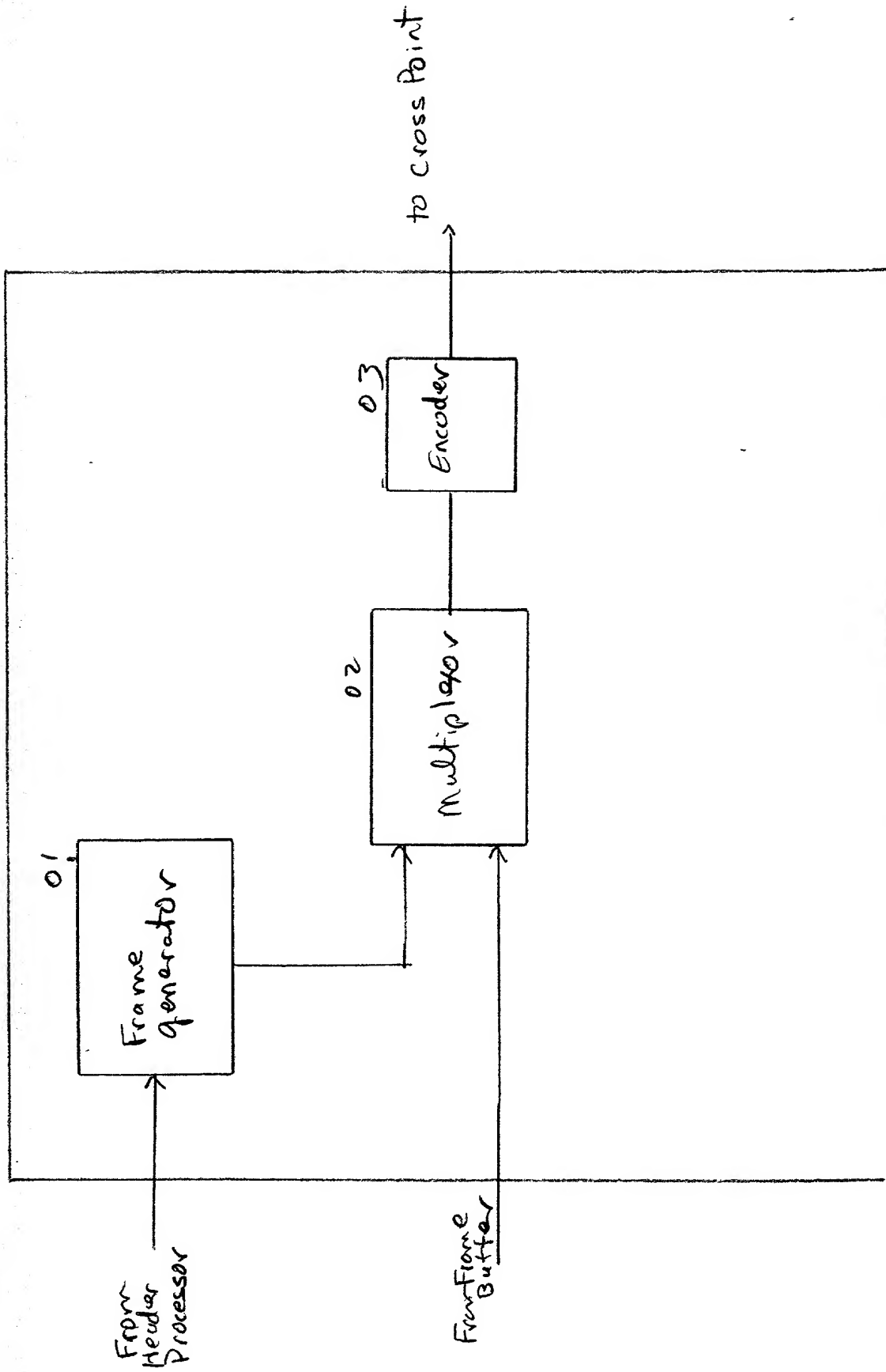


Fig 6

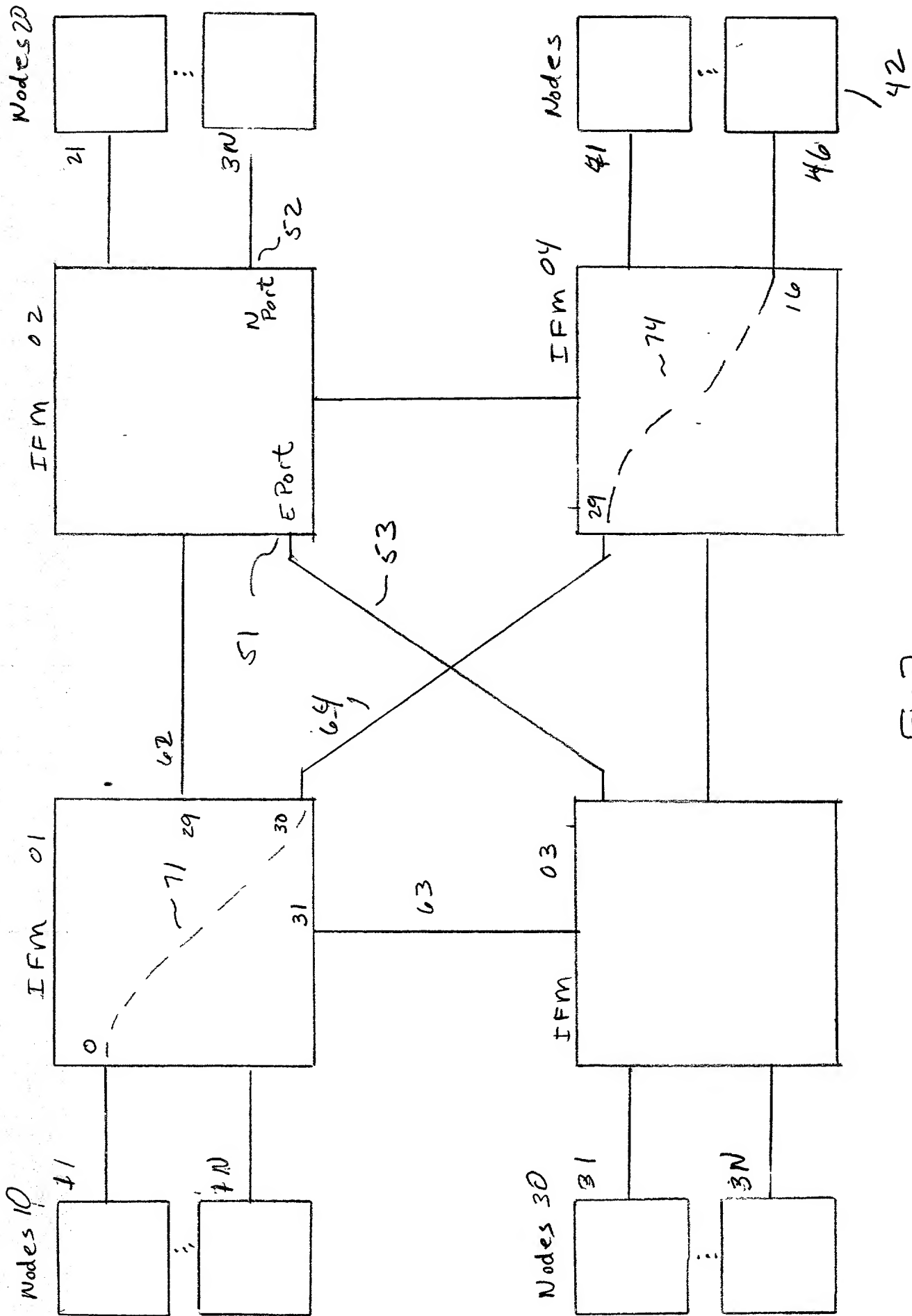


FIG. 10

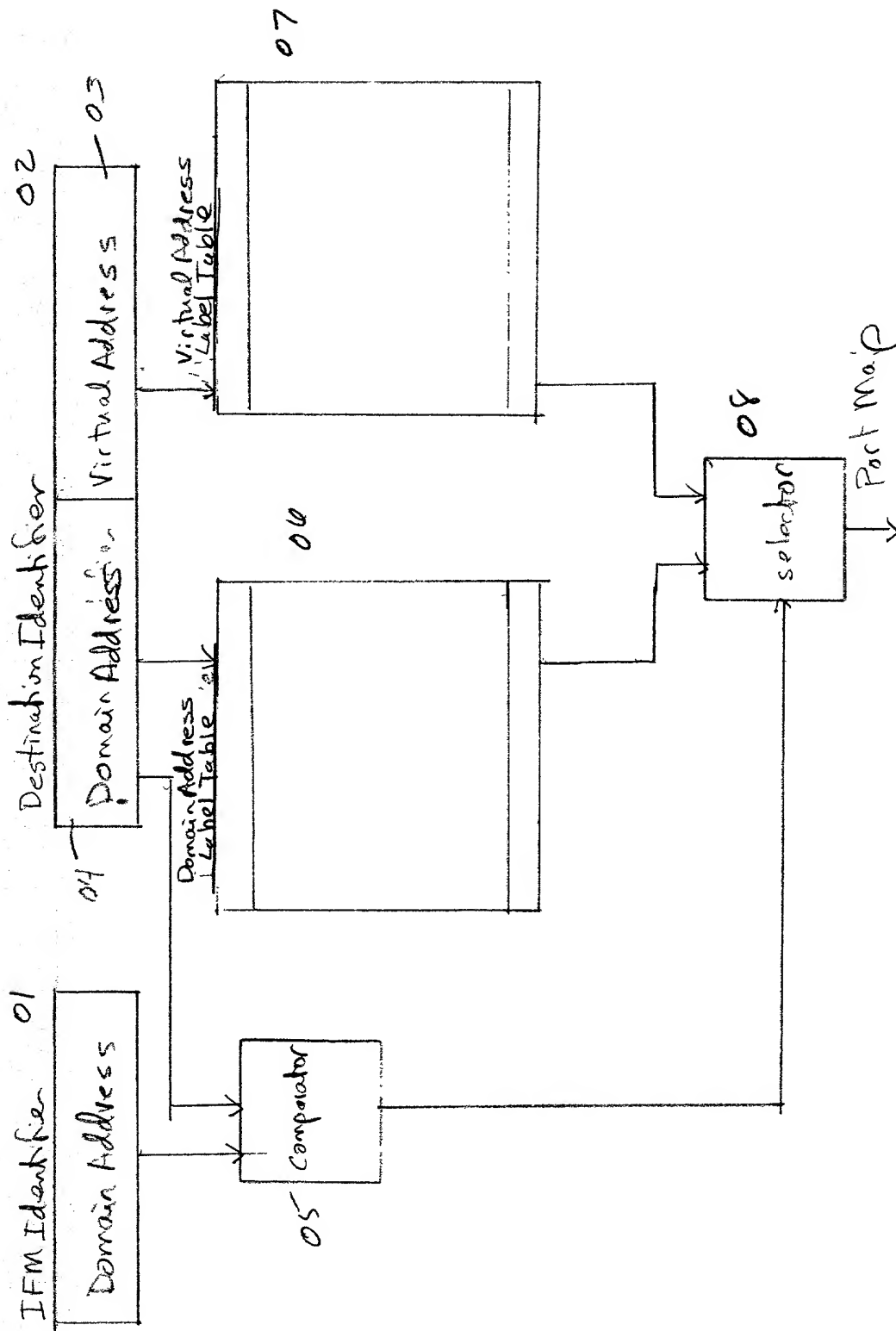


Fig. 8

Quad Switch Protocol Controller

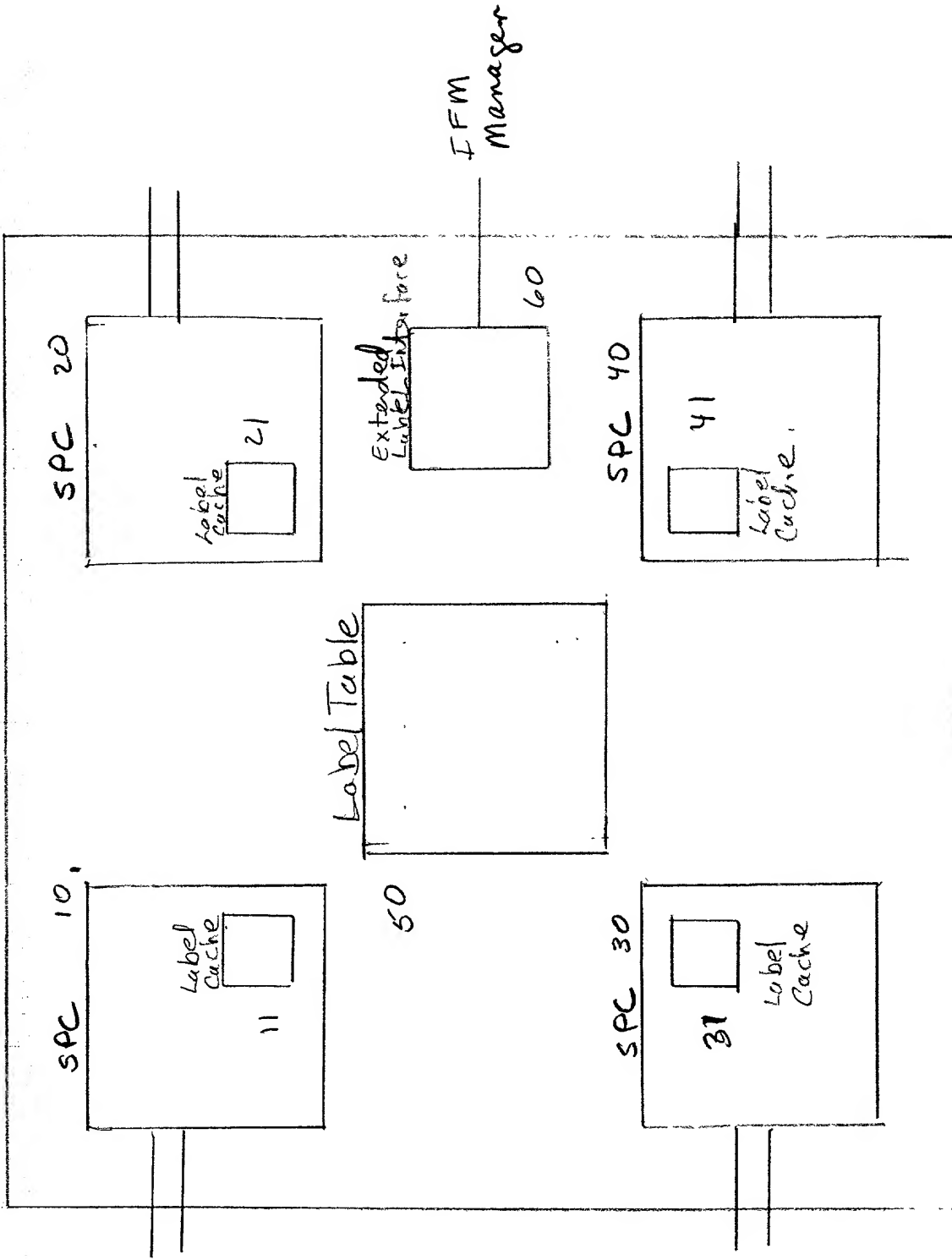


Fig 9

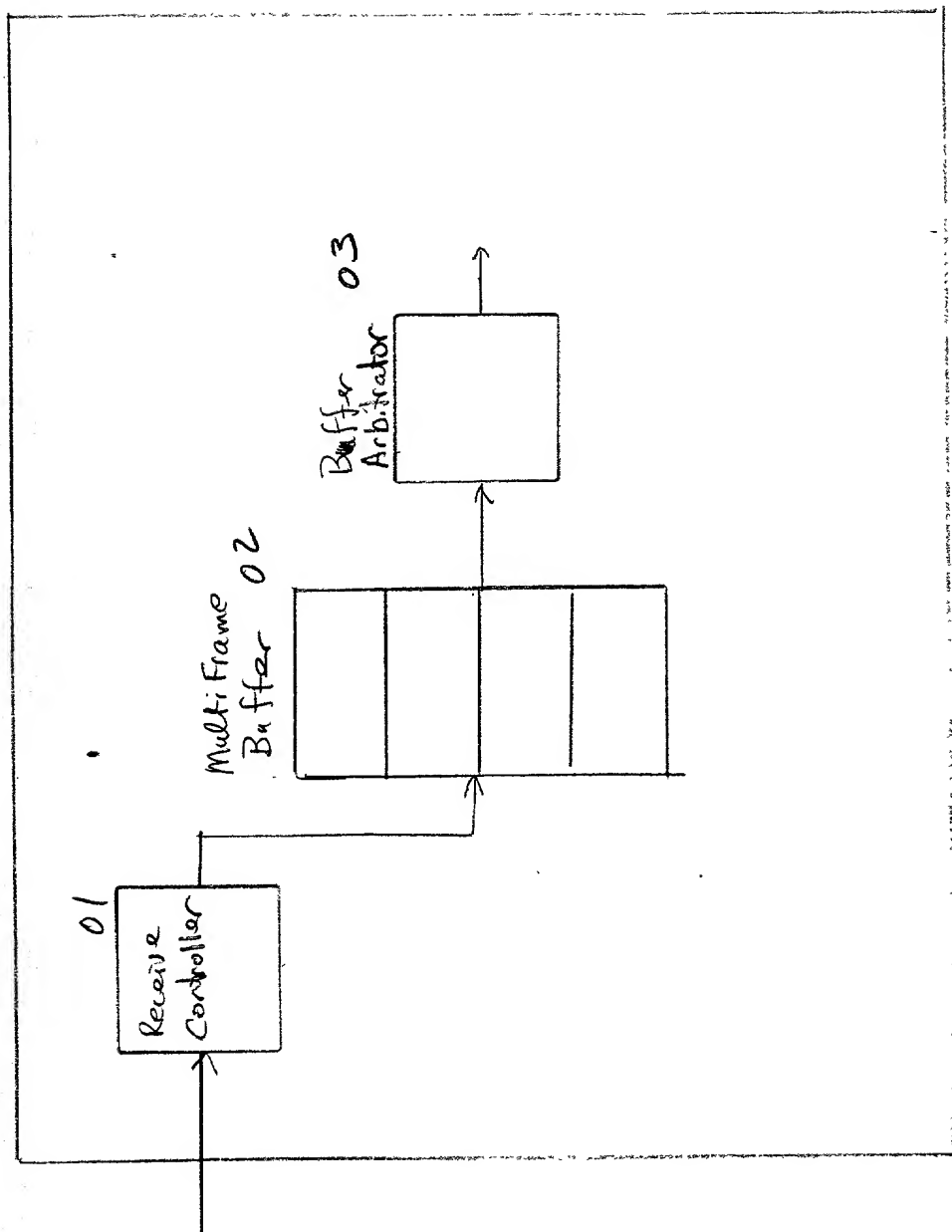


FIG 10

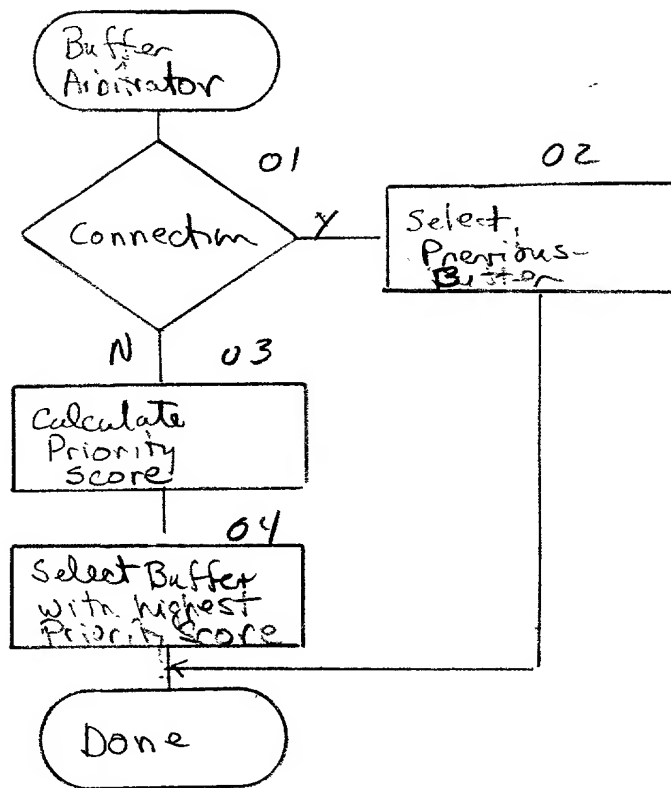


Fig 11

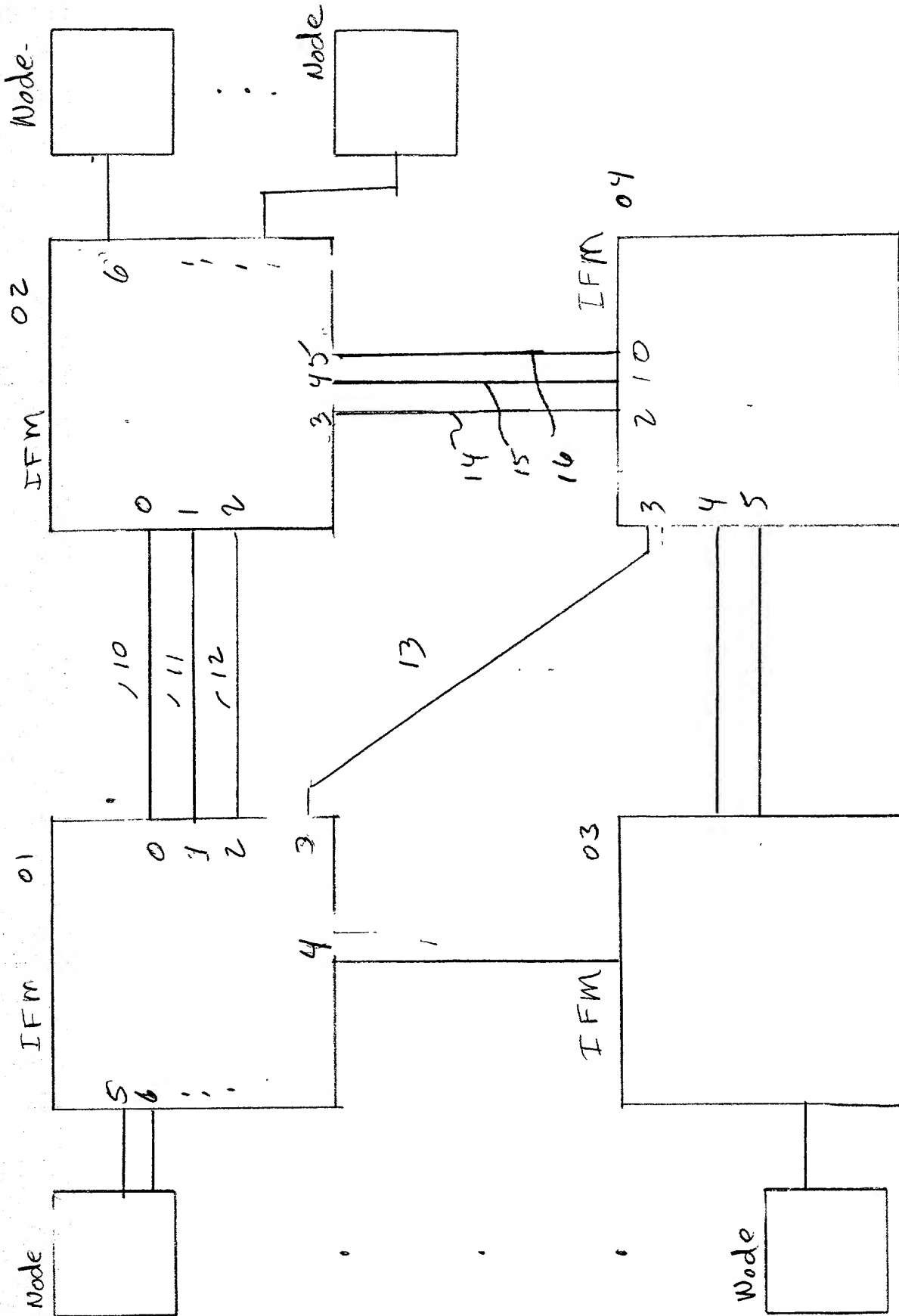


Fig 12

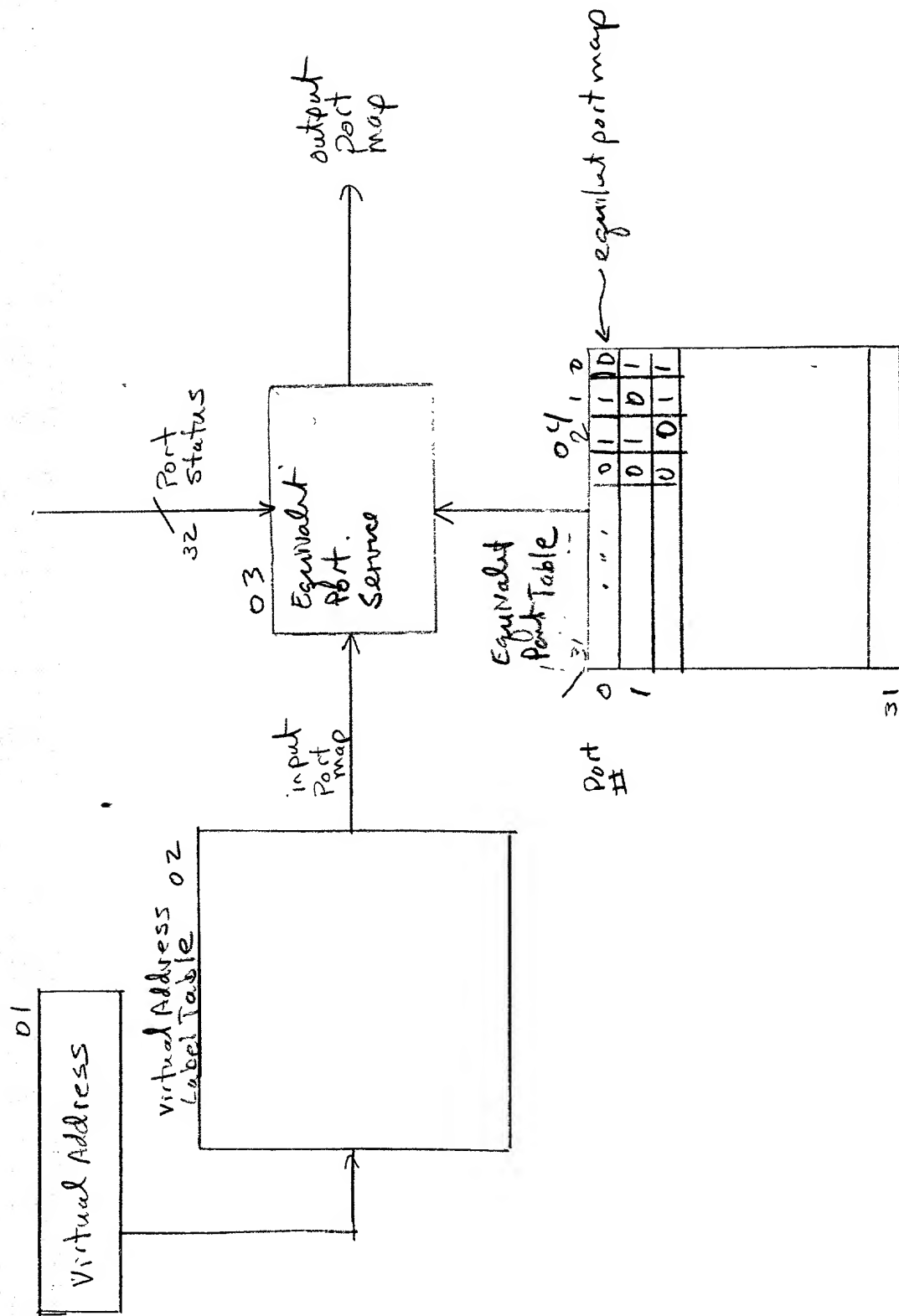


Fig 13

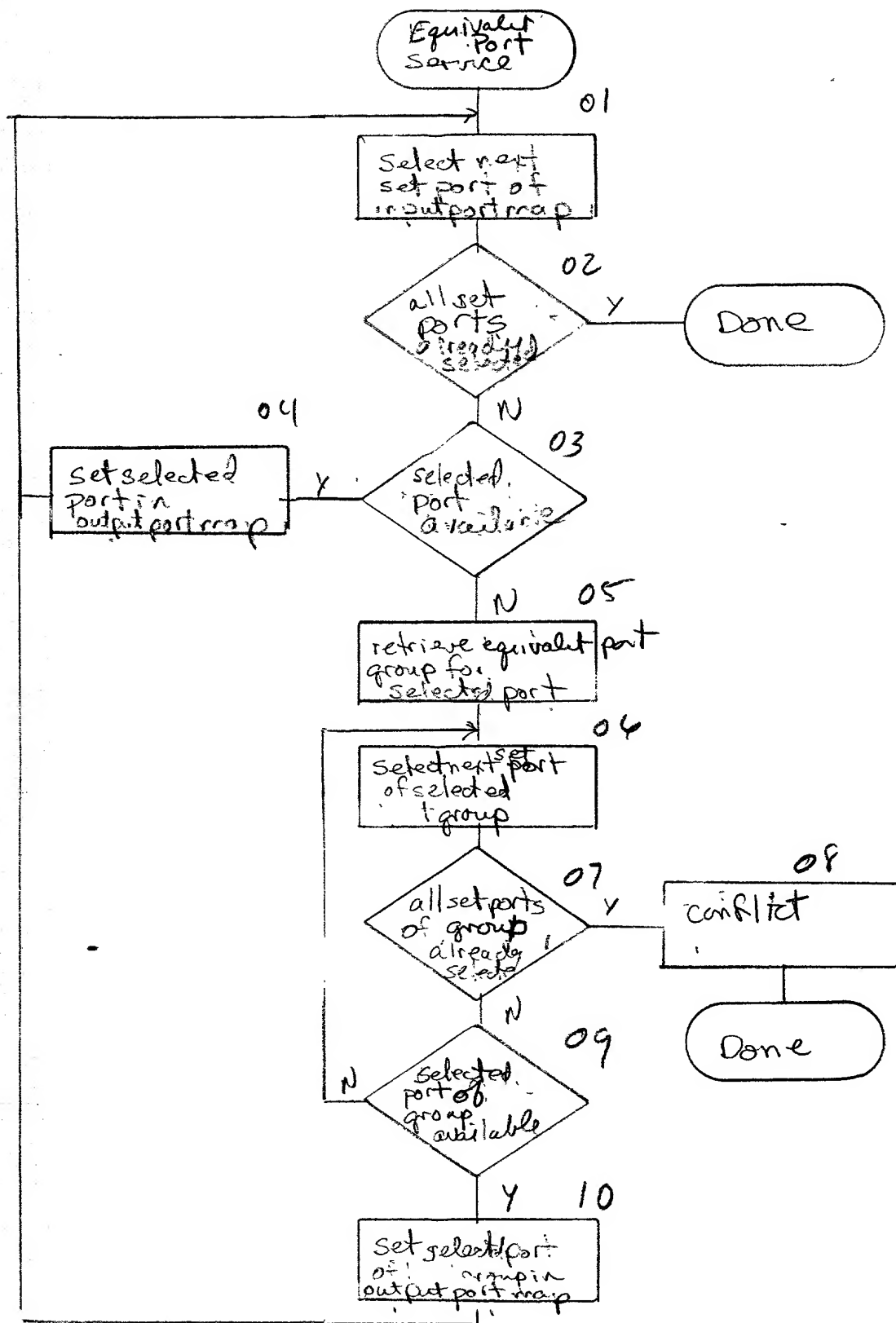


Fig 14

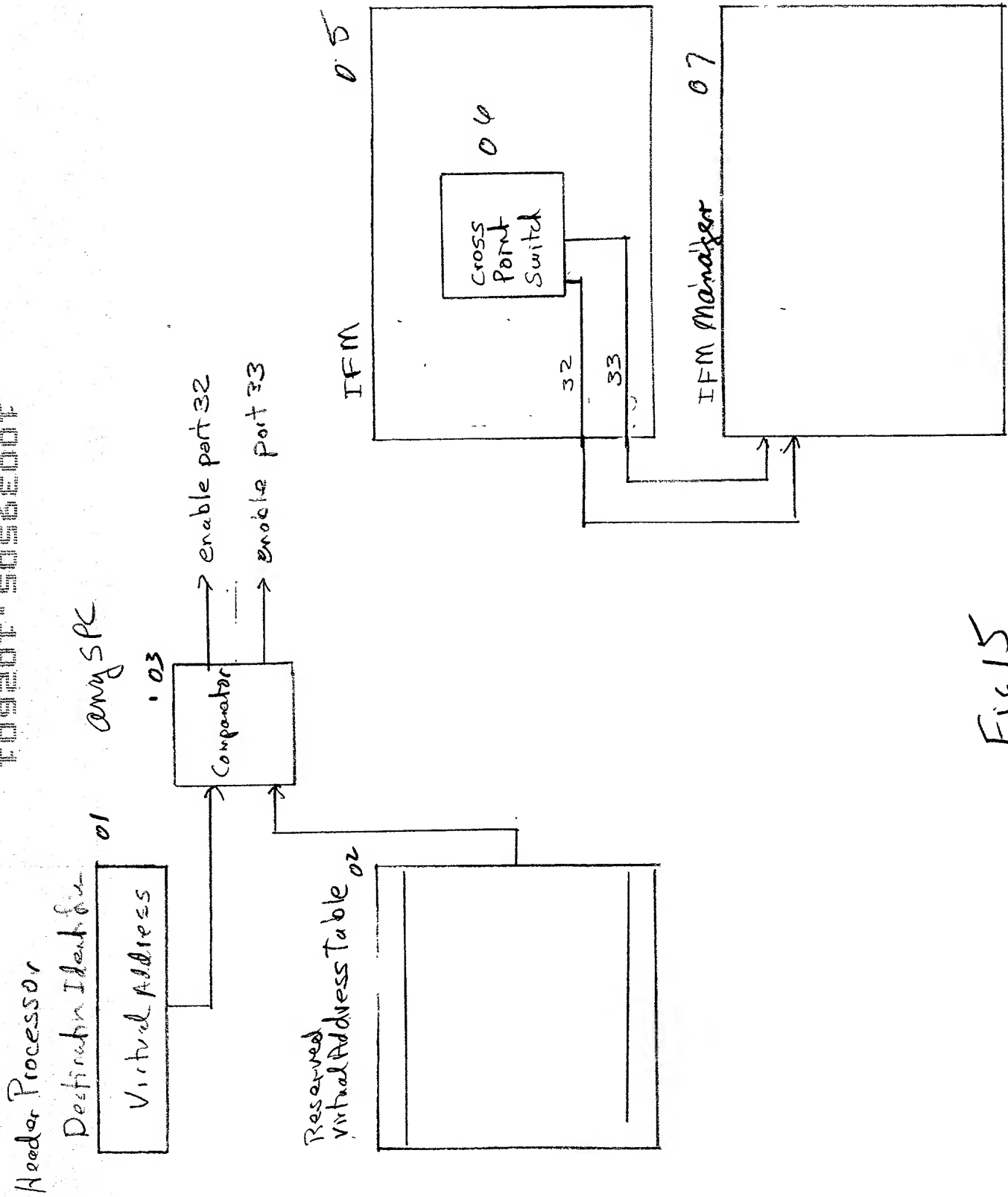
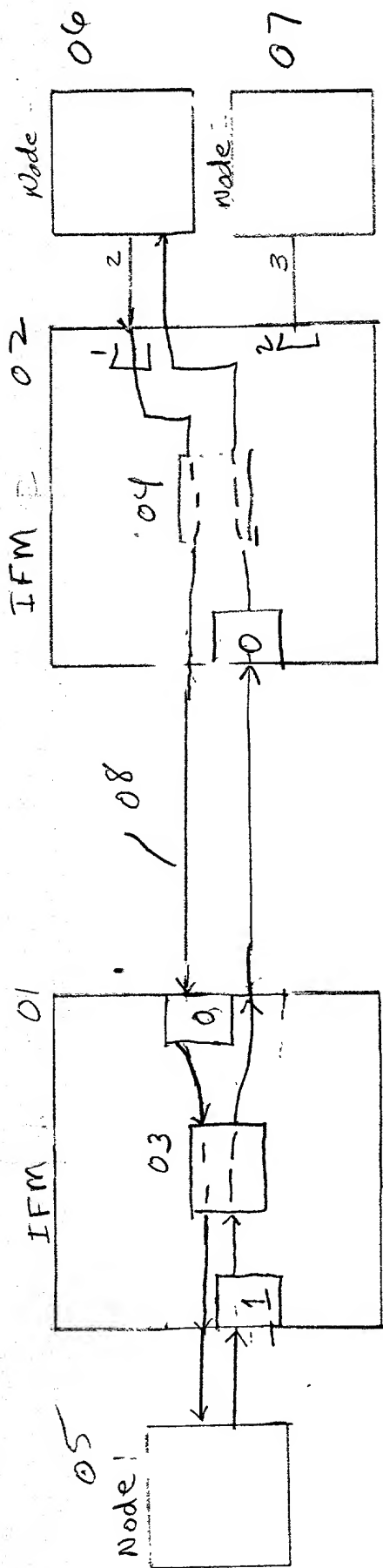


Fig 15



Deadlock

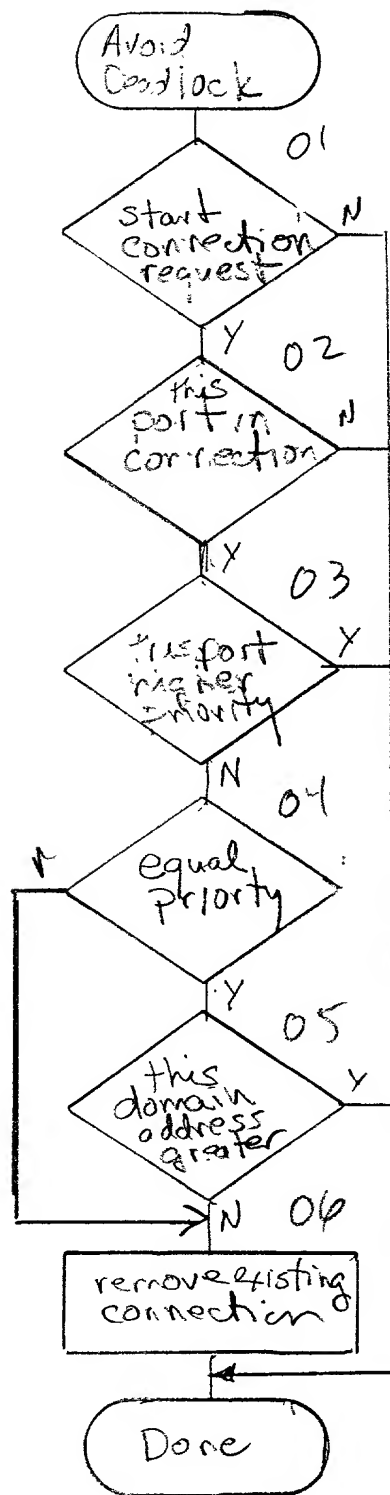
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Time Node 1605 IFM 1601 Node 1606 IFM 1602

0 send start connect 1 2 send start connect

1	connect 1 ↔ 0	connect 2 ↔ 0
2	Forward start connect	Forward start connect
3	can't forward start connect Node 1	can't forward start connect

Fig 16



When end-to-end
not established

Fig 17

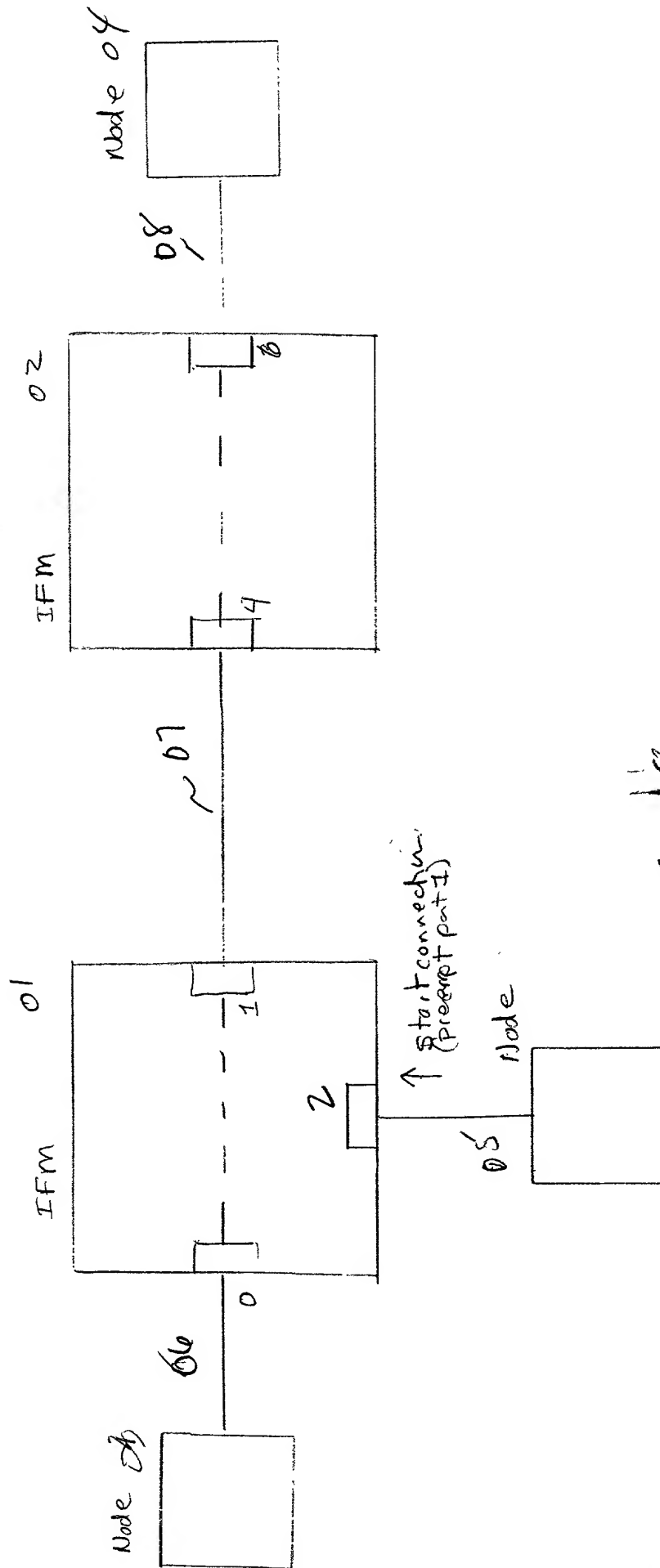


Fig 18

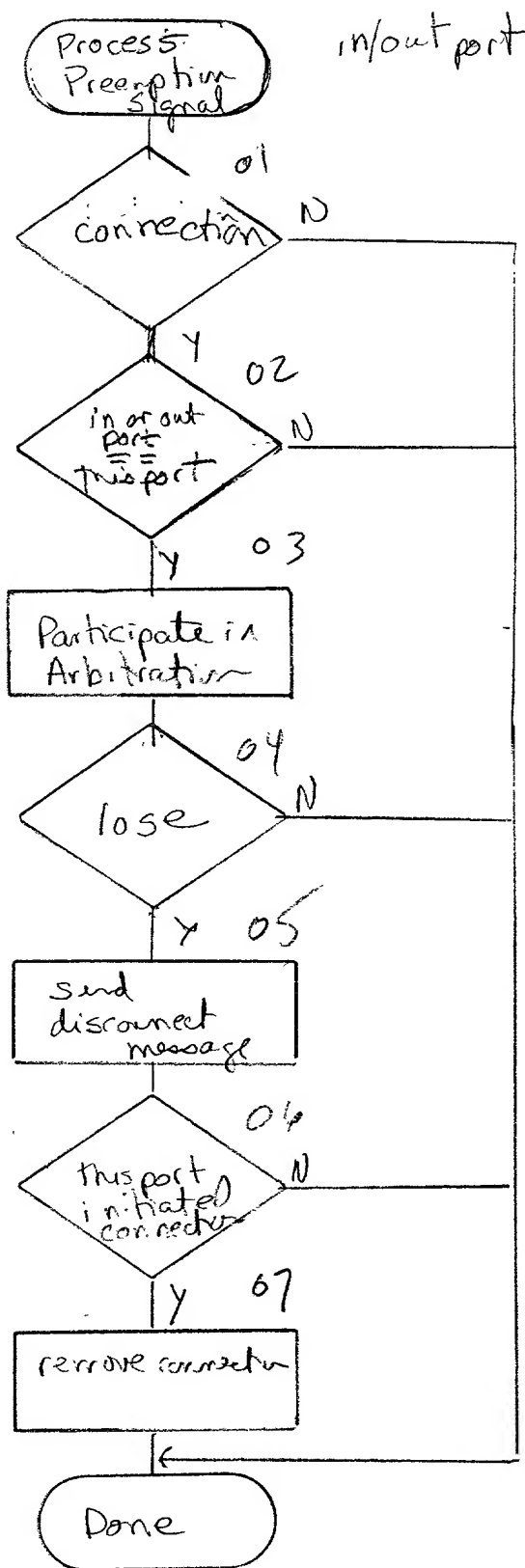


Fig. 19

10039505-102601

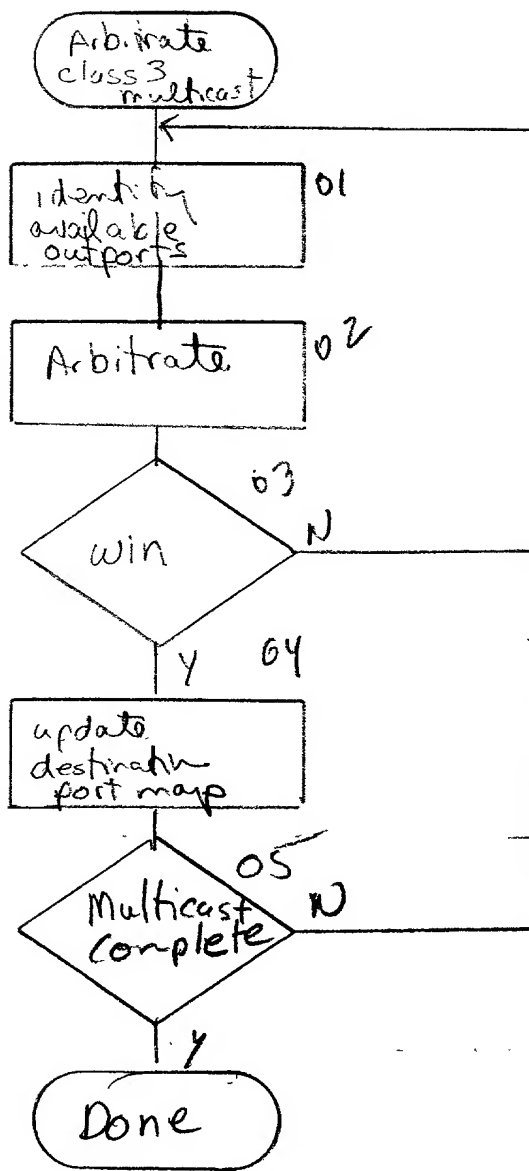


Figure 20